Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	20	("5781715").PN. OR ("6078963"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/26 00:28
L1	2	port adj processor and routing adj processor and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/25 23:48
S121	33	primary adj4 processor with secondary adj4 processor$1 and "370"/$.ccls. and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/23 23:16
S119	1	routing near3 (primary with secondary) adj3 processor$1 and "370"/$.ccls. and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/23 22:34
S117	4	routing near3 (first with second) adj3 processor$1 and "370"/$.ccls. and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/23 22:34
S116	158	routing near3 processors and "370"/$.ccls. and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:55
S115	5	fib with rib and (@rlaD<"20000505" @ad<"20000505") and "370"/$. ccls.	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:55
S113	0	(fib rib) same processors and "370"/$.ccls. and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:54
S110	1	fib with port$1 and rib with (processor$1) and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:51
S109	1	fib with (cpu controller processor$1) and (cpu controller processor$1) with rib and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:51
S108	1	fib with processor$1 and (processor$1) with rib and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:50

S10 6	7	port$1 with processor$1 and (port$1 processor$1) with fib and (@rlaD<"20000505" @ad<"20000505")	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:50
S10 4	1	port near3 processor and processor$1 with fib	US-PGPUB; USPAT	OR	OFF	2005/10/22 19:30